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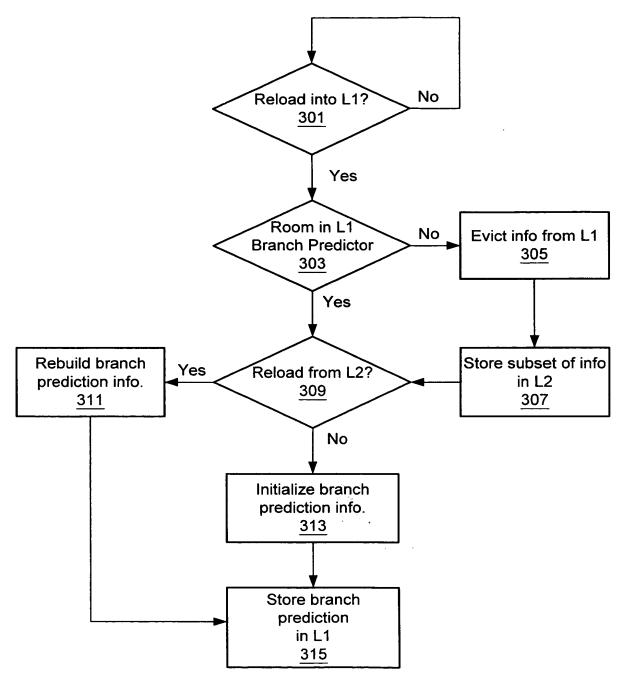
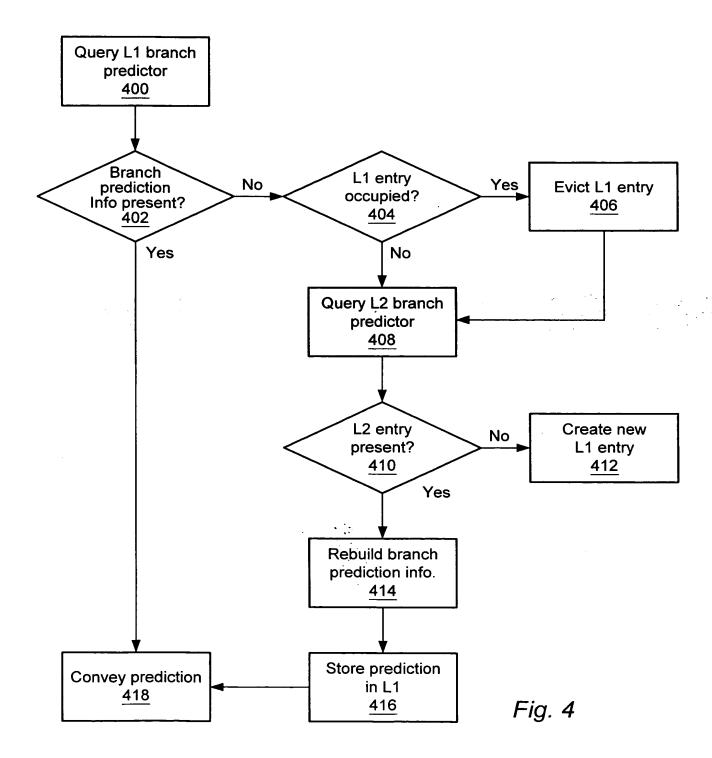
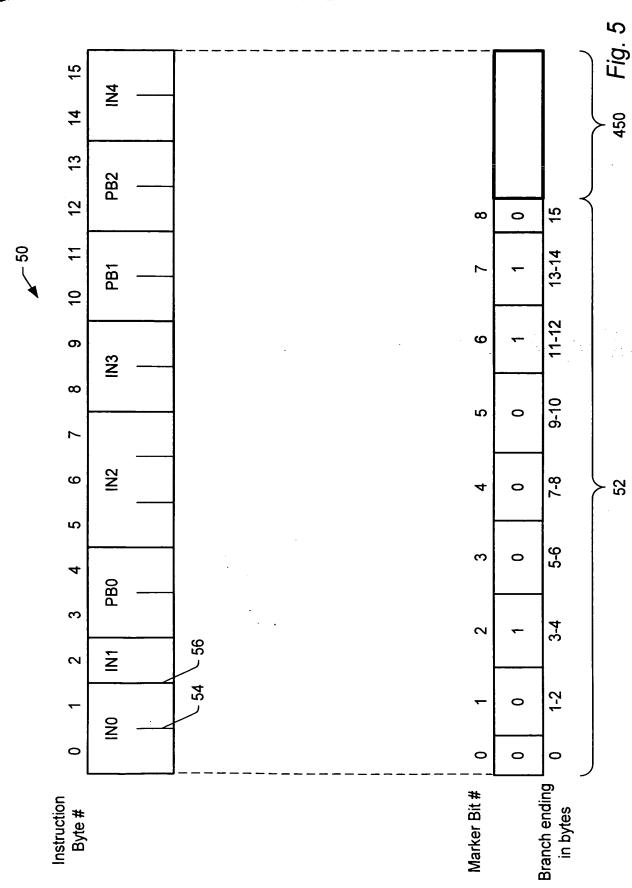


Fig. 3





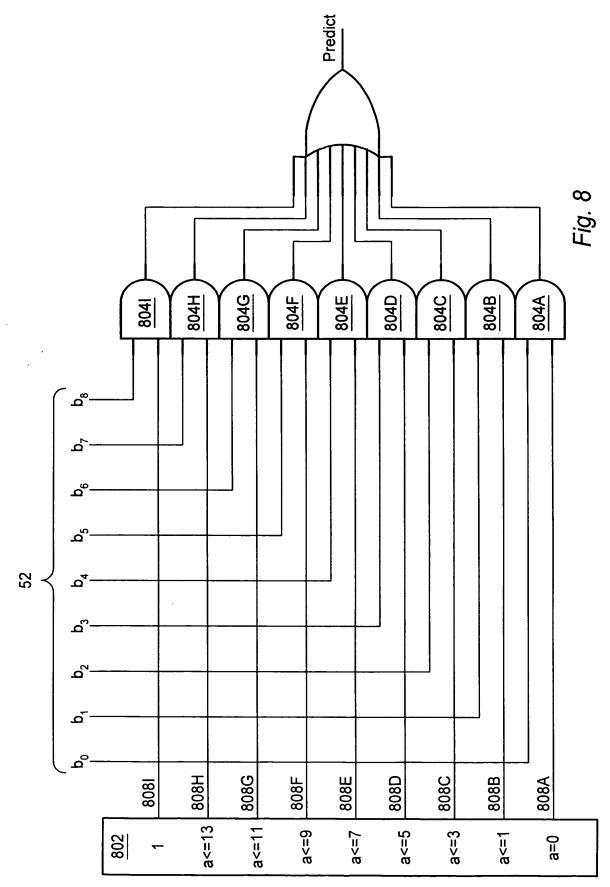




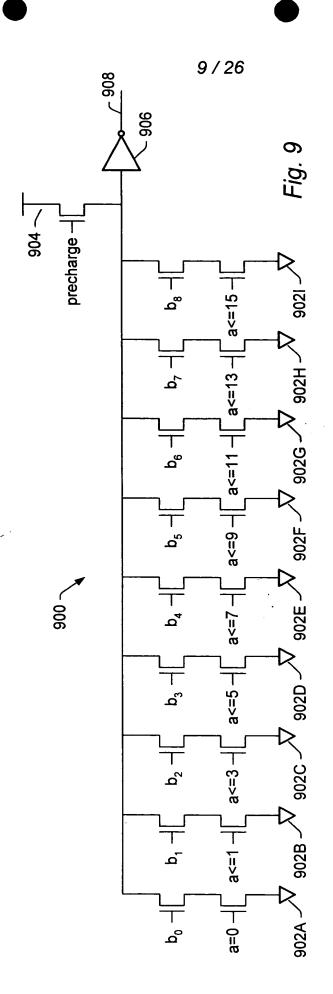
9	602 \ 6	604	909	608	610	612	614	616	618
Offset	0=>	-> 	<=3	<=5	/=>	6=>	<=11	<=13	<=15
Marker Bit #		Υ	7	က	4	2	9	2	ω

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15	IN4		 		8	0	15	<=15	Fig. 7
4	_				7	1	13-14	<=13	Fig
13	PB2						~	v	_
1 12					9	-	11-12	<=11	
11	PB1						- -		_
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6	IN3								
ω	_				4	0	7-8	<i>L</i> =>	
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Instruction Byte #	<u>707</u>				Marker Bit #	704	Branch ending in bytes	Offset 708	

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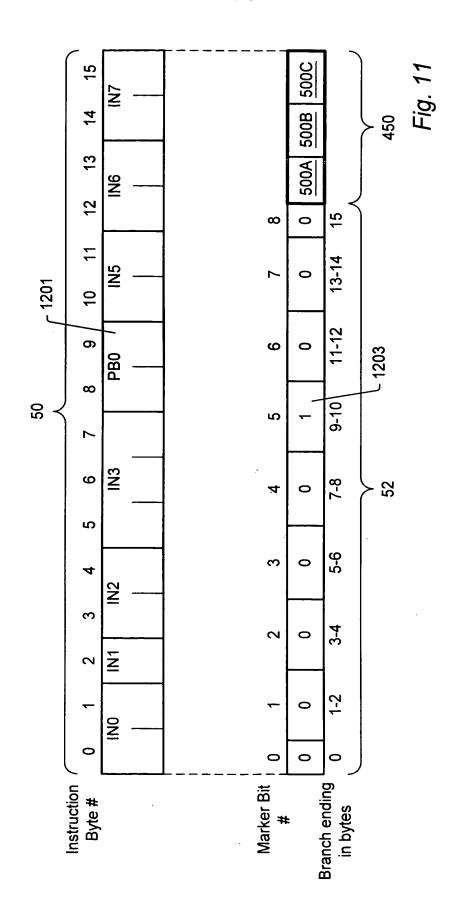






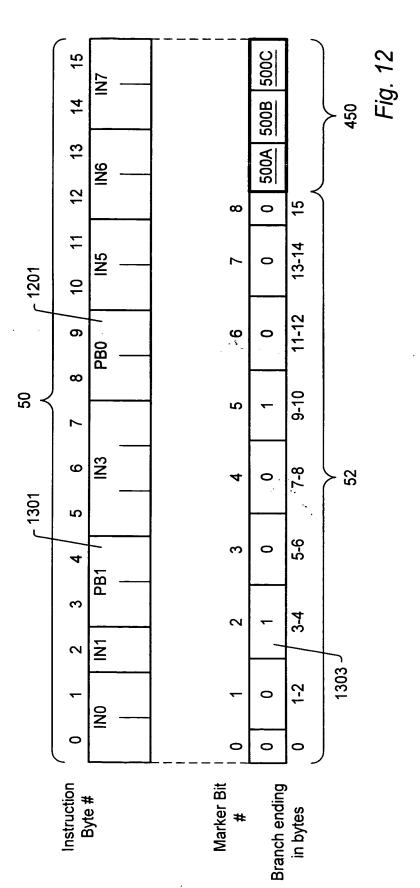
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	Instruction (E		-	Marker Bit		Branch ending = in bytes		



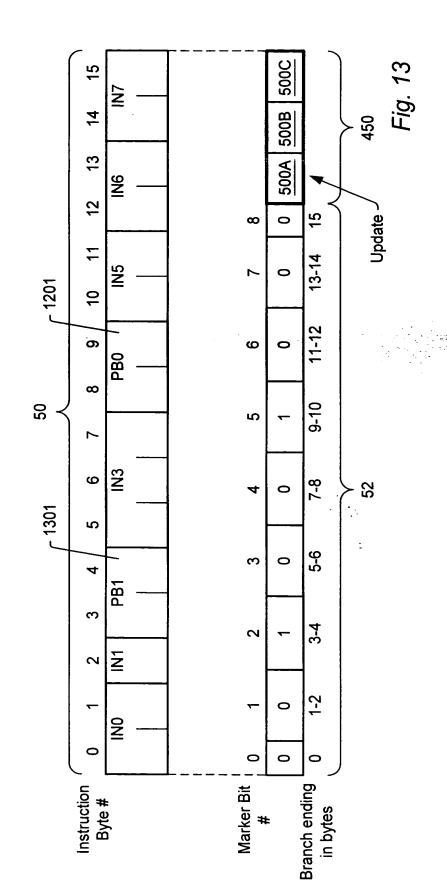


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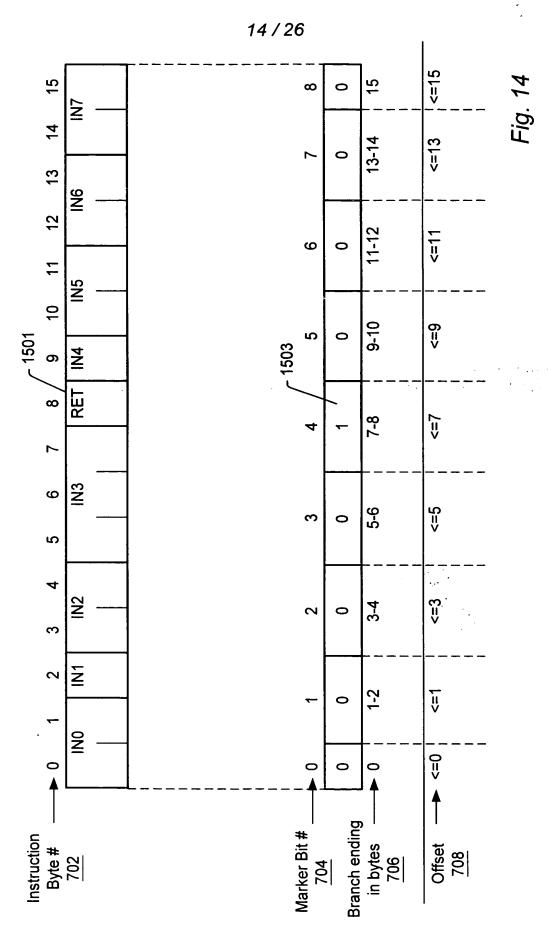






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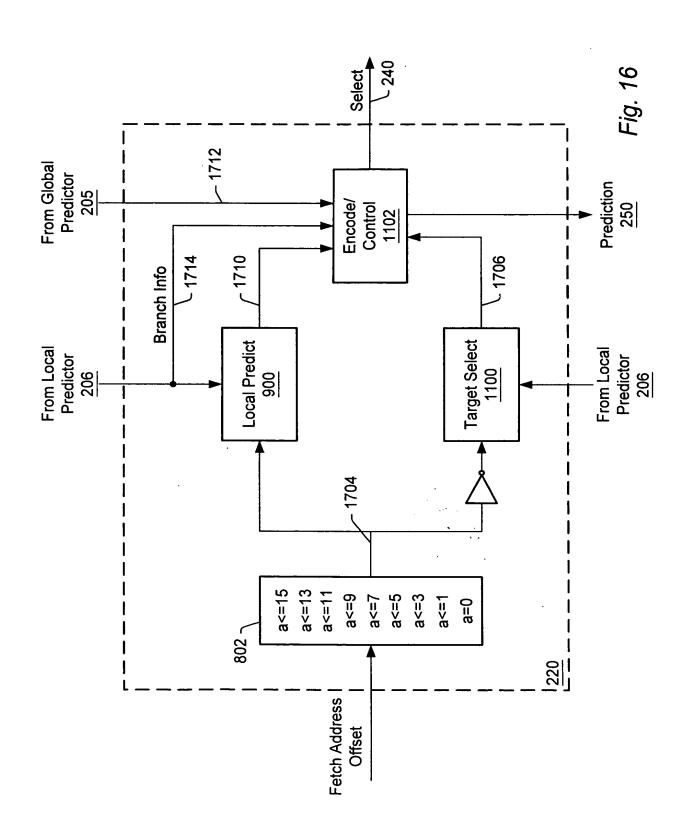
<u>8</u>

call

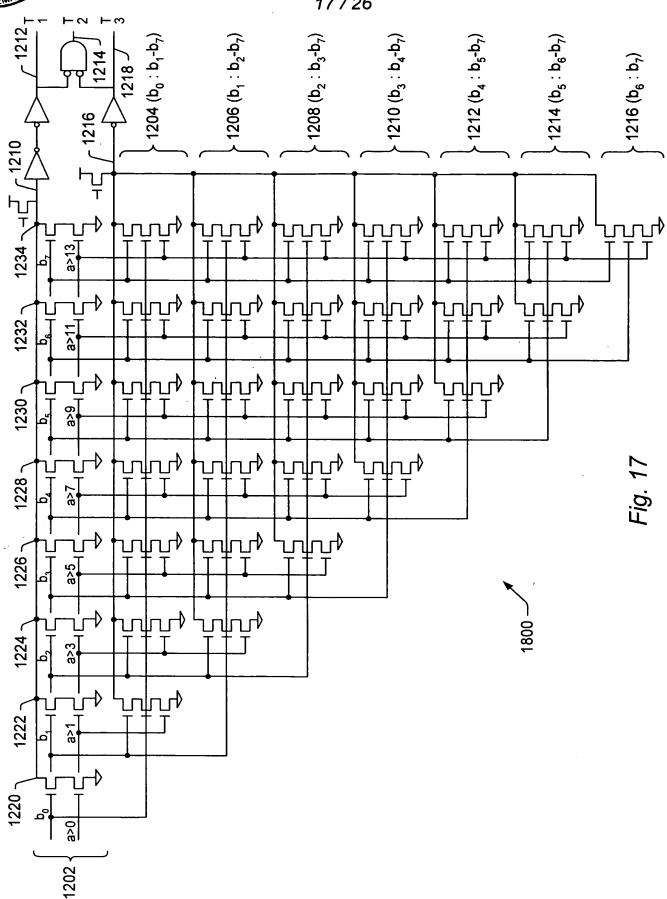
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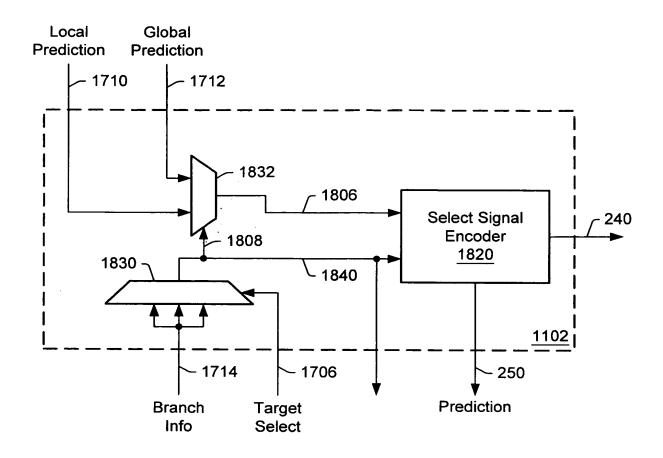
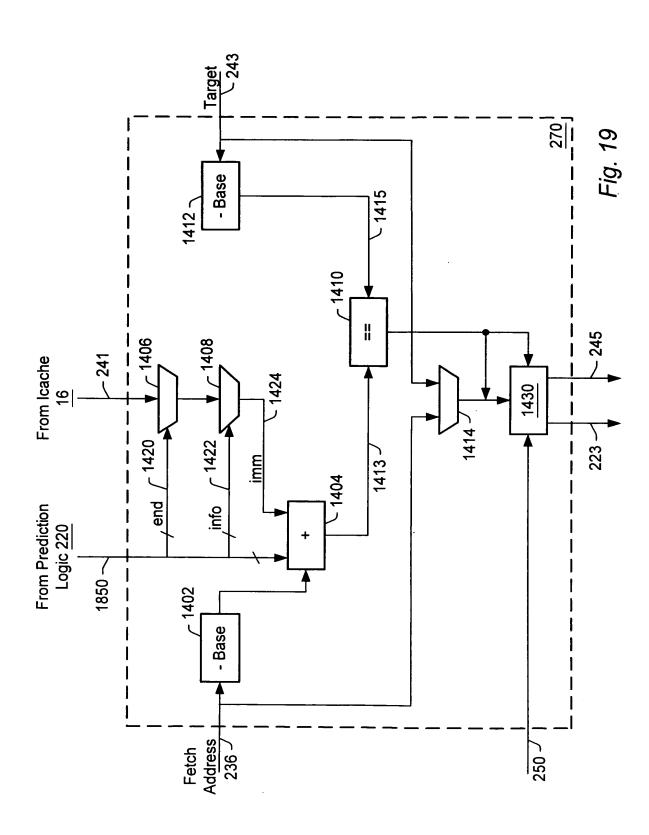


Fig. 18







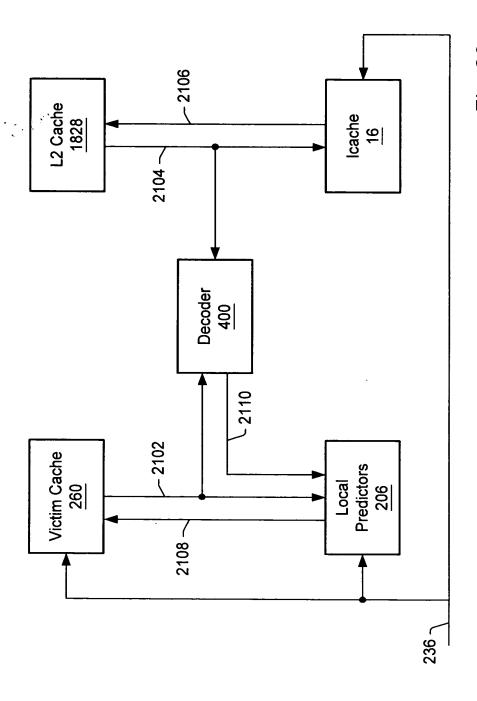


Fig. 20



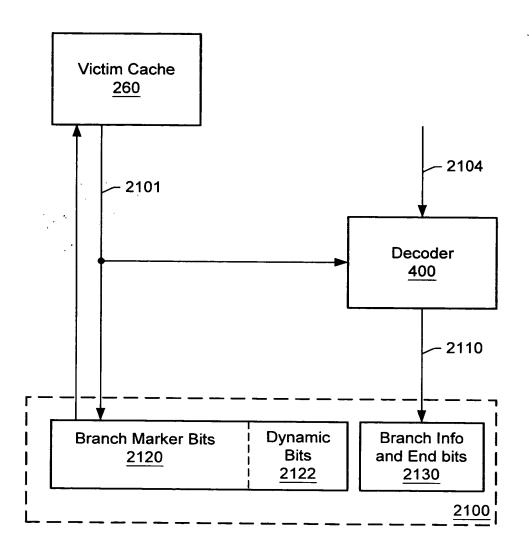


Fig. 21



